

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	988	(320/132).CCLS	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2009/07/02 19:11
L2	0	L1 and (op& (different\$4 with amplifier)) and (constant with voltage) and (direct with current with amplifier) and (A/D rectifier AC/DC) and (filter with (band\$pass)) and impedance and (micro\$process\$4 cpu micro\$controller controller processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2009/07/02 19:14
L3	0	L1 and (op& (different\$4 with amplifier)) and (constant with voltage) and (direct with current with amplifier) and ((A adj1 D) rectifier (AC\$DC)) and (filter with (band\$pass)) and impedance and (micro\$process\$4 cpu micro\$controller controller processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2009/07/02 19:15
L4	0	L1 and (op& ((operational different\$4) with amplifier)) and (constant with voltage) and (direct with current with amplifier) and ((A adj1 D) rectifier (AC \$DC)) and (filter with (band\$pass)) and impedance and (micro \$process\$4 cpu micro \$controller controller processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2009/07/02 19:16

L5	0	L1 and (op\$amp ((operational different\$4) with amplifier)) and (constant with voltage) and (direct with current with amplifier) and ((A adj1 D) rectifier (AC \$DC)) and (filter) and impedance and (micro\$process\$4 cpu micro\$controller controller processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2009/07/02 19:16
L6	2	L1 and (op\$amp ((operational different\$4) with amplifier)) and (constant with voltage) and (direct with current with amplifier) and (filter) and impedance and (micro\$process\$4 cpu micro\$controller controller processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2009/07/02 19:17
L7	940	(320/134).COLS	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2009/07/02 19:17
L8	2	L6 and (op\$amp ((operational different\$4) with amplifier)) and (constant with voltage) and (direct with current with amplifier) and (filter) and impedance and (micro\$process\$4 cpu micro\$controller controller processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2009/07/02 19:17
L9	18240	("320").CLAS	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2009/07/02 19:17
L10	5	L9 and (op\$amp ((operational different\$4) with amplifier)) and (constant with voltage) and (direct with current with amplifier) and (filter) and impedance and (micro\$process\$4 cpu micro\$controller controller processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2009/07/02 19:17

L11	1	L9 and ((op\$amp ((operational different\$4) with amplifier)) and (constant with voltage) and (direct with current with amplifier) and (filter) and impedance and (micro\$process\$4 cpu micro\$controller controller processor)). clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2009/07/02 19:17
L12	1	((op\$amp ((operational different\$4) with amplifier)) and (constant with voltage) and (direct with current with amplifier) and (filter) and impedance and (micro \$process\$4 cpu micro \$controller controller processor)). clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2009/07/02 19:18
L13	872	(320/137).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2009/07/02 19:19
L14	416	(320/135).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2009/07/02 19:20
L15	1	((op\$amp ((operational different\$4) with amplifier)) and (constant with voltage) and (direct with current with amplifier) and (filter) and impedance and (micro \$process\$4 cpu micro \$controller controller processor)). clm.	US-PGPUB	OR	ON	2009/07/02 19:24
S1	2	("20020161537").FN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2008/07/14 16:00
S2	883	(320/132).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2008/07/14 17:20

S3	8	S2 and impedance and filter and (op\$amp (operational with amplif \$4)) and (convert\$4 A \$1D AC\$1DC)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2008/07/14 17:45
S4	1	("6842708").PN.	USPAT; USOCR	OR	OFF	2008/07/14 18:13
S5	15	("4743831" "5565759" "5751217" "6087808" "6108579" "6232750" "6259232" "6448743" "6538449" "6563318" "6608469" "6642719").PN. OR ("6842708").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/14 18:14
S6	783	(320/134).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2008/07/14 18:14
S7	9	S6 and impedance and filter and (op\$amp (operational with amplif \$4)) and (convert\$4 A \$1D AC\$1DC)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2008/07/14 18:15
S8	9	S6 and imped\$4 and filter and (op\$amp (operational with amplif \$4)) and (convert\$4 A \$1D AC\$1DC)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2008/07/14 18:16
S9	21	S6 and filter and (op \$amp (operational with amplif\$4)) and (convert \$4 A\$1D AC\$1DC)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2008/07/14 18:38
S10	17	S6 and filter and (op \$amp (operational with amplif\$4)) and (convert \$4 AC\$DC)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2008/07/14 18:40
S11	16784	("320").CLAS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2008/07/14 18:55
S12	244	S11 and filter and (op \$amp (operational with amplif\$4)) and (convert \$4 AC\$DC)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2008/07/14 18:55

S13	127	S11 and filter and (op \$amp (operational with amplifi\$4)) and (convert \$4 AC\$DC) and (micro \$processor micro \$computer micro \$computer cpu)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2008/07/14 18:58
S14	125	S13 and (voltage with (monitor\$4 sens\$4 measur\$4 detect\$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2008/07/14 19:05
S15	25	S14 and (mux multiplex \$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2008/07/14 19:33
S16	15	S14 and diode with (constant with voltage)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2008/07/14 21:24
S17	16796	("320").CLAS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2008/07/15 09:12
S18	228	S17 and (zener with diode) with (constant with voltage)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2008/07/15 09:12
S19	16796	("320").CLAS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2008/07/15 14:46
S20	23	S19 and ((zener with diode) with (constant with voltage)) same (current with limit\$4 with resistor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2008/07/15 14:46
S21	17562	("320").CLAS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2009/02/02 16:17
S22	54	S21 and (band\$pass with filter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2009/02/02 16:17

S23	19	S21 and (high\$pass with filter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2009/02/02 16:33
S24	39	S21 and ((high adj pass) with filter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2009/02/02 16:33
S25	69	S21 and (((high adj pass) (low adj pass) (band adj pass)) with filter) and ((operational with amplifier) op\$samp) and (convert\$4) and (cpu microprocess\$4 micro\$control\$4 process \$4 control\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2009/02/02 16:36
S26	12	S21 and (((high adj pass) (low adj pass) (band adj pass)) with filter) and ((operational with amplifier) op\$samp) and (convert\$4) and (cpu microprocess\$4 micro\$control\$4 process \$4 control\$4) and ((measur\$4 monitor\$4 detect\$4 sens\$4) with impedance)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2009/02/02 16:36

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